

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A circuit ~~substrate-substrate~~, comprising:  
                    a substrate;  
a plurality of terminals formed on ~~a~~the substrate; and  
at least one or more resistances-resistance formed between ~~said~~the terminals adjacent one ~~to~~ another;  
  
    ~~wherein said~~the plurality of terminals ~~include~~including analog terminals connected to analog signal lines ~~for supplying to~~supply analog signals, and digital terminals connected to digital signal lines ~~for supplying to~~supply digital signals; and  
  
    and wherein saidone resistance ~~which has~~having at least one end thereof connected to ~~said~~the analog terminal, ~~has~~and having a resistance value greater than ~~said~~another resistance connected between ~~said~~the digital terminals.
2. (Currently Amended) A circuit ~~substrate-substrate~~, comprising:  
                    a substrate;  
a plurality of terminals formed on ~~a~~the substrate; and  
at least one or more resistances-resistance formed between ~~said~~the terminals adjacent one ~~to~~ another;  
  
    ~~wherein said~~the plurality of terminals ~~include~~including first terminals connected to data lines ~~for supplying to~~supply data signals, and second terminals connected to control lines ~~for supplying to~~supply control signals; and  
  
    and wherein saidone resistance ~~which has~~having at least one end thereof connected to ~~said~~the first terminal, ~~has~~and having a resistance value greater than ~~said~~another resistance connected between ~~said~~the second terminals adjacent one ~~to~~ another.

3. (Currently Amended) A circuit ~~substrate-substrate~~, comprising:

a substrate;  
a common electrode line formed on ~~the~~ a perimeter of ~~a~~ the substrate;  
a plurality of terminals formed on ~~said~~ the substrate; and  
at least one or more resistances-resistance formed between ~~said~~ the terminals  
and ~~said~~ the common electrode line;

~~wherein~~ said the plurality of terminals ~~include~~ including analog terminals  
connected to analog signal lines ~~for supplying to supply~~ analog signals, and digital terminals  
connected to digital signal lines ~~for supplying to supply~~ digital signals; and  
~~and wherein~~ said one resistance connected to ~~said~~ the analog terminal ~~has~~  
having a resistance value greater than ~~said~~ another resistance connected to ~~said~~ the digital  
terminal.

4. (Currently Amended) A circuit ~~substrate-substrate~~, comprising:

a substrate;  
a common electrode line formed on ~~the~~ a perimeter of ~~a~~ the substrate;  
a plurality of terminals formed ~~of~~ said on the substrate;  
at least one or more first resistances-resistance formed between ~~said~~ the  
terminals adjacent one ~~to~~ another; and  
at least one or more second resistances-resistance formed between ~~said~~ the  
terminals and ~~said~~ the common electrode line.

5. (Currently Amended) ~~A~~ The circuit substrate according to Claim 4, ~~wherein~~  
~~said~~ the terminal ~~is being~~ connected to both ~~said~~ the first resistance and ~~said~~ the second  
resistance; and

~~and wherein~~ said the first resistance ~~has~~ having a resistance value greater than  
~~said~~ the second resistance.

6. (Currently Amended) ~~A~~ The circuit substrate according to Claim 5, ~~wherein~~ ~~said the~~ plurality of terminals ~~include~~ including analog terminals connected to analog signal lines ~~for supplying to supply~~ analog signals, and digital terminal connected to digital signal lines ~~for supplying to supply~~ digital signals; and

~~and wherein~~ both ~~said the~~ first resistance and ~~said the~~ second resistance which have at least one end ~~thereof~~ connected to ~~said the~~ analog terminal, ~~have~~ having resistance values greater than both ~~said the~~ first resistance which is connected between ~~said the~~ digital terminals, and ~~said the~~ second resistance which is connected between ~~said the~~ digital terminal and ~~said the~~ common electrode line.

7. (Currently Amended) ~~A~~ The circuit substrate according to Claim 1, further comprising:

electric power terminals connected to a power source; and

resistances formed between ~~said the~~ electric power terminals and adjacent non-electric power terminals formed for purposes other than supplying power.

8. (Currently Amended) ~~A~~ The circuit substrate according to Claim 7, ~~wherein~~ ~~said the~~ resistance ~~has~~ having a resistance value equal to or less than the resistance connected to other non-electric power terminals.

9. (Currently Amended) A circuit ~~substrate-substrate~~, comprising:

a substrate;

a common electrode line formed on ~~the a~~ perimeter of ~~a the~~ substrate;

data line terminals connected to the data lines ~~for supplying to supply~~ analog signals;

control signal terminals connected to control signal lines ~~for supplying to~~ supply digital signals;

electric power terminals ~~for supplying to~~ supply at least one of negative electric power ~~or~~ and positive electric power;

first resistances connected between ~~said the~~ terminals adjacent one ~~to~~ another;  
and

second resistances connected between ~~said the~~ terminals.

10. (Currently Amended) A ~~The~~ circuit substrate according to Claim 9, ~~wherein~~, in the event that any of ~~said the~~ terminals are connected to both ~~said the~~ first resistance and ~~said the~~ second resistance, ~~said the~~ first resistance ~~has~~ having a resistance value greater than ~~said the~~ second resistance.

11. (Currently Amended) A ~~The~~ circuit substrate according to Claim 10, ~~wherein~~ both ~~said the~~ first resistance and ~~said the~~ second resistance which have at least one end ~~thereof~~ connected to ~~said the~~ data terminal, ~~have~~ having resistance values greater than any of ~~said the~~ first resistance connected between ~~said the~~ control signal terminals, ~~said the~~ first resistance connected between ~~said the~~ control signal terminal and ~~said the~~ electric power terminal, ~~said the~~ second resistance connected between ~~said the~~ control signal terminal and ~~said the~~ common electrode line, and ~~said the~~ second resistance connected between ~~said the~~ electric power terminal and ~~said the~~ common electrode line.

12. (Currently Amended) A ~~The~~ circuit substrate according to Claim 11, ~~wherein~~ ~~said the~~ resistances ~~are~~ being formed of a semiconductor film.

13. (Currently Amended) A ~~The~~ circuit substrate according to Claim 1, ~~wherein~~ ~~said the~~ resistance ~~includes~~ including a protection circuit configuration employing PN junction configurations with reverse polarity.

14. (Currently Amended) An electro-optical ~~device including~~ device, comprising:  
thea circuit substrate according to Claim 1.

15. (Currently Amended) An electronic ~~apparatus including~~apparatus,  
comprising:

~~\_\_\_\_\_an the~~ electro-optical device according to Claim 14.

16. (Currently Amended) A manufacturing method for a circuit substrate  
~~including that includes~~ a common electrode line on ~~the a~~ a perimeter ~~thereof of the~~ substrate,  
and a plurality of terminals on ~~the an~~ an inner side of ~~said the~~ said substrate from ~~said the~~ said common  
electrode line, the method comprising:

~~a step for forming at least one or more first resistance configurations~~  
configuration on regions between ~~said the~~ said terminals adjacent one to another;

~~a step for forming at least one or more second resistance configurations~~  
configuration on regions between ~~said the~~ said terminals and ~~said the~~ said common electrode line;

~~a step for forming said the~~ said terminals which are electrically connected to a part  
of ~~said at least one of the first resistance configurations configuration or/and said the second~~  
resistance configurations configuration; and

~~a step for forming said the~~ said common electrode lines which are electrically  
connected to a part of ~~said the second resistance configurations configuration.~~

17. (Currently Amended) ~~A The~~ The manufacturing method for a circuit substrate  
according to Claim 16, ~~wherein said the first resistance configurations configuration and said~~  
the second resistance configurations are configuration being formed so that ~~said the first~~  
resistance configuration has a resistance value greater than ~~said the second resistance~~  
configuration.